

**FIG. 2B**  
side view

dielectric layer 14

**FIG. 2A**  
front view

dielectric layer 14

**FIG. 1B**  
side view

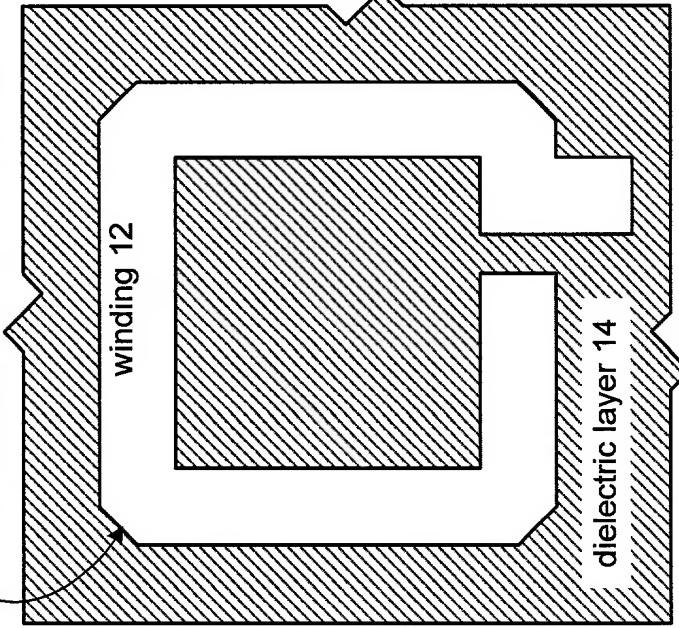
dielectric layer 14

**FIG. 1A**  
front view

**on-chip inductor 10**

geometric shape 16

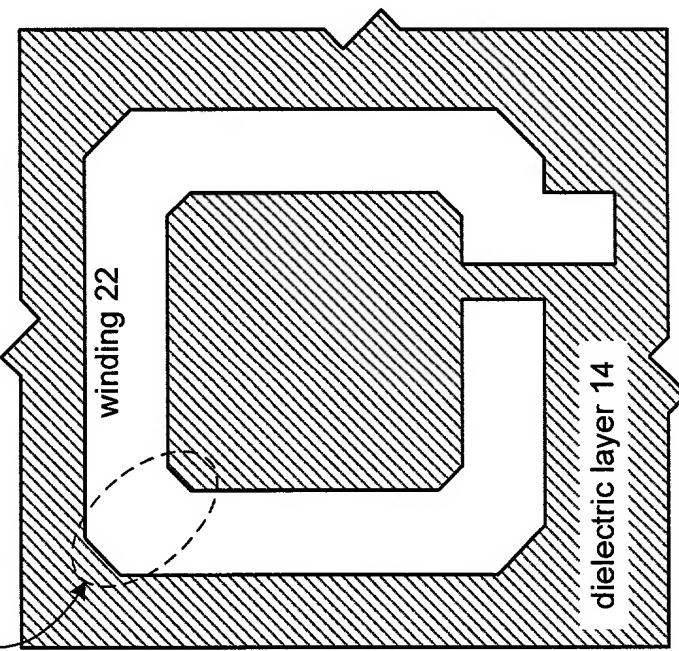
winding 12



**on-chip inductor 20**

geometric shape 26

winding 22



on-chip inductor 30

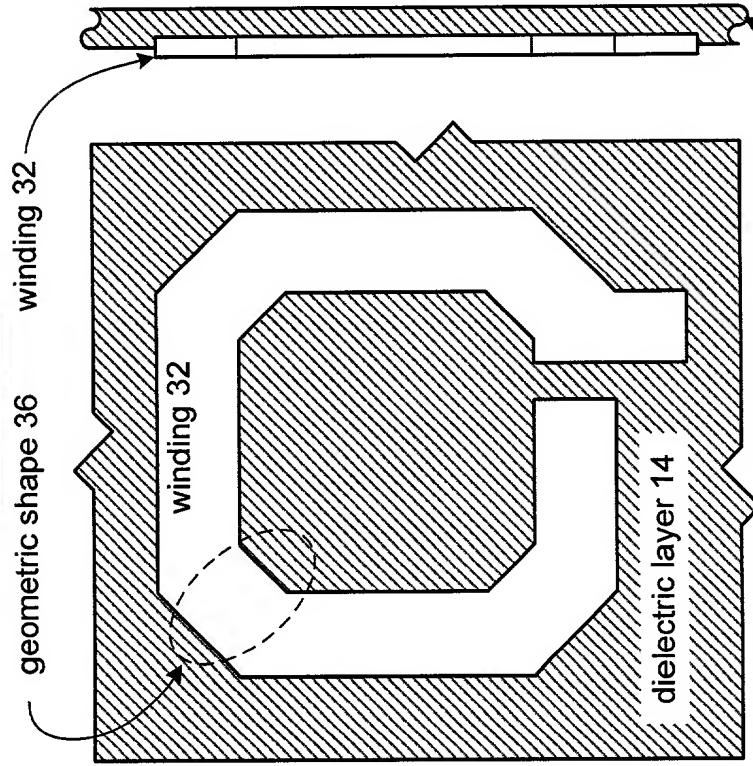


FIG. 3A  
front view

FIG. 3B  
side view

on-chip inductor 40

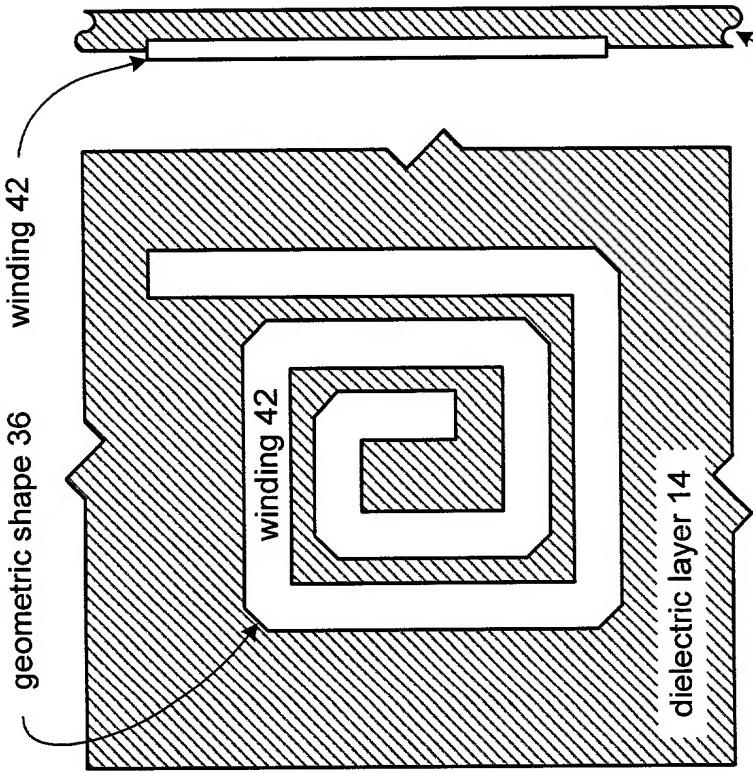
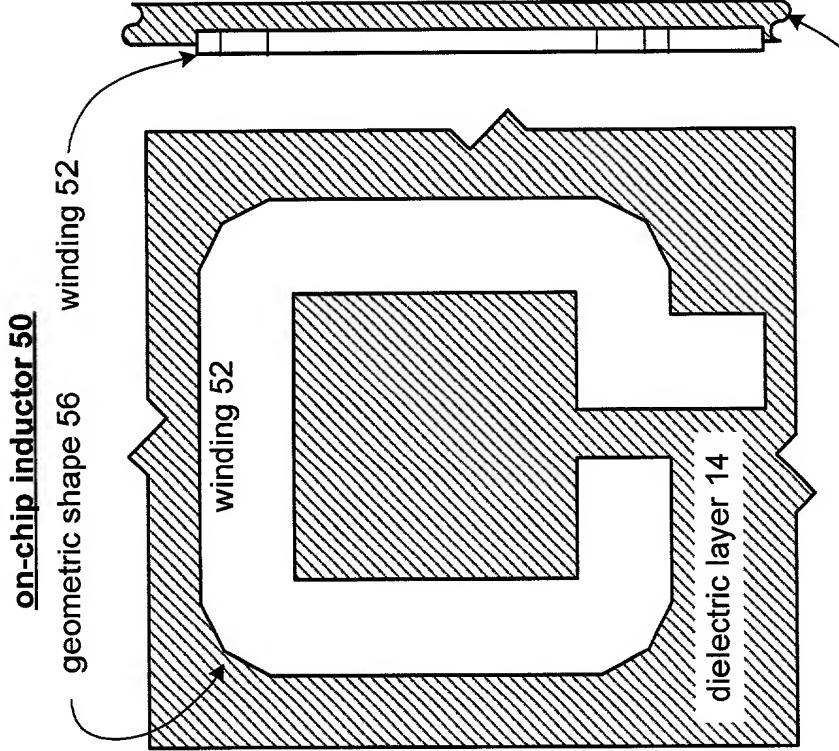
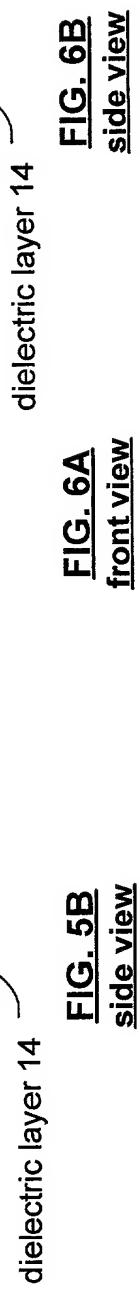


FIG. 4A  
front view

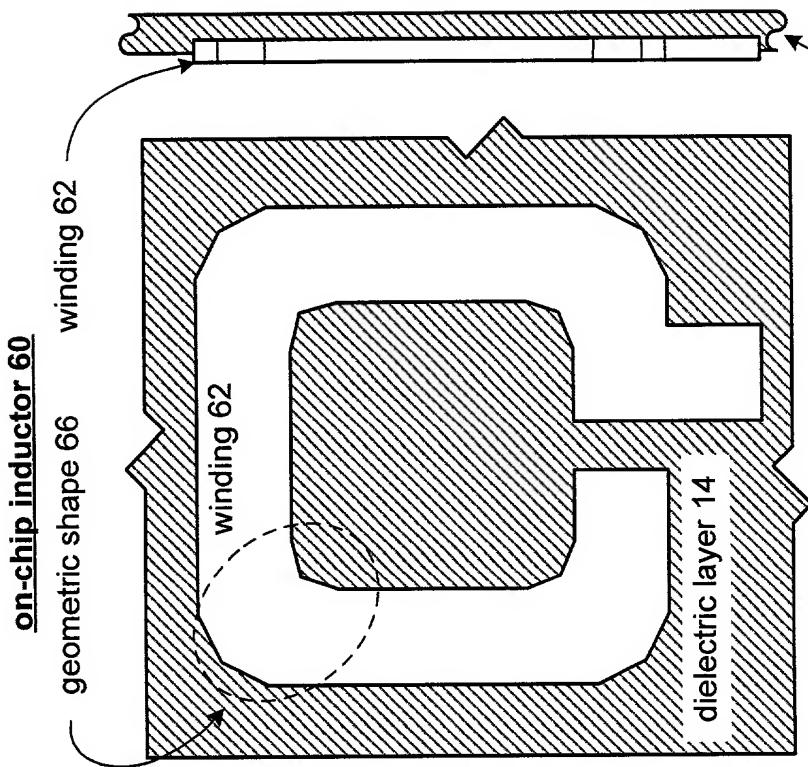
FIG. 4B  
side view



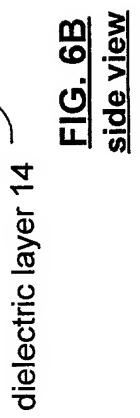
**FIG. 5A**  
front view



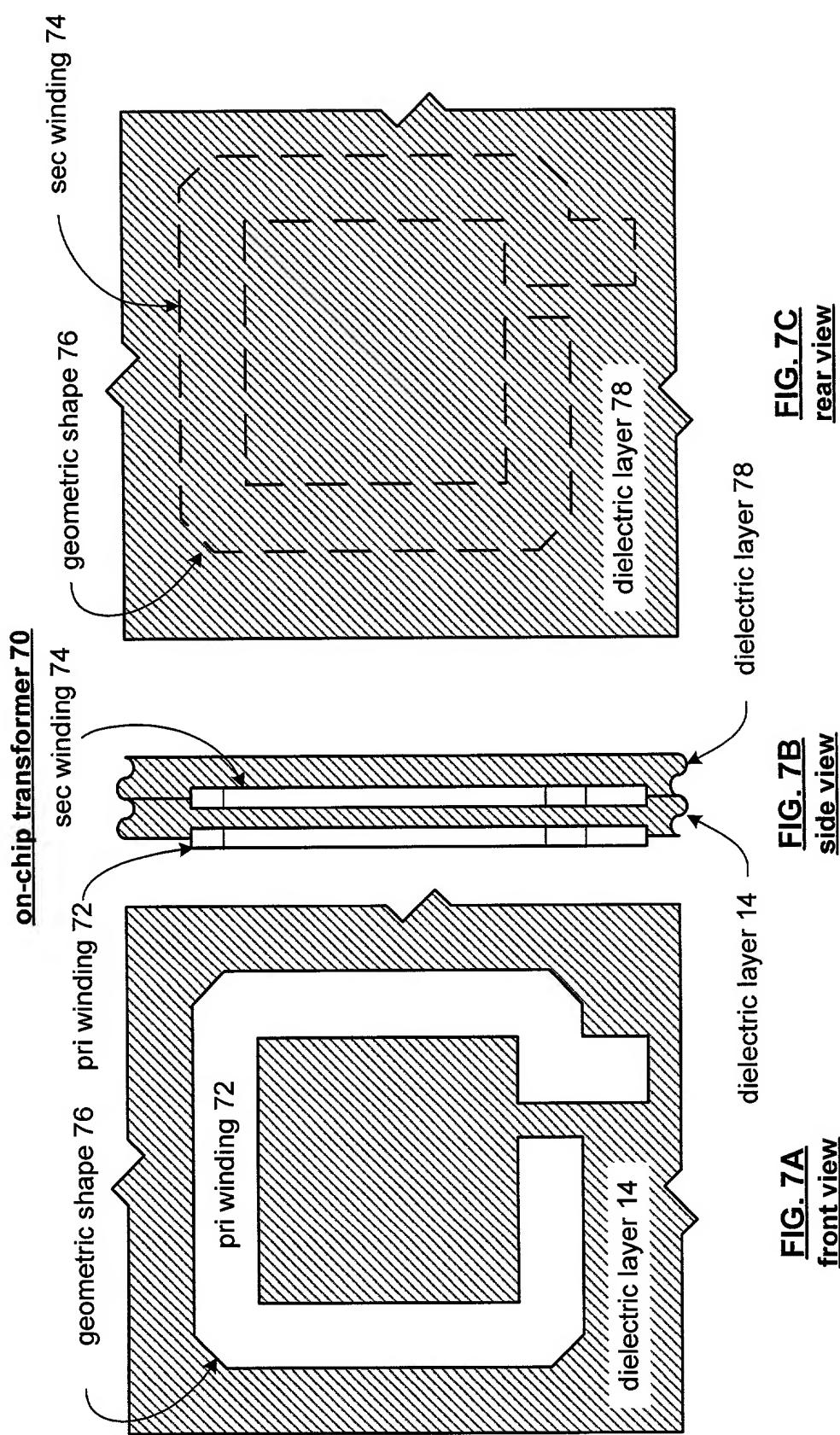
**FIG. 5B**  
side view



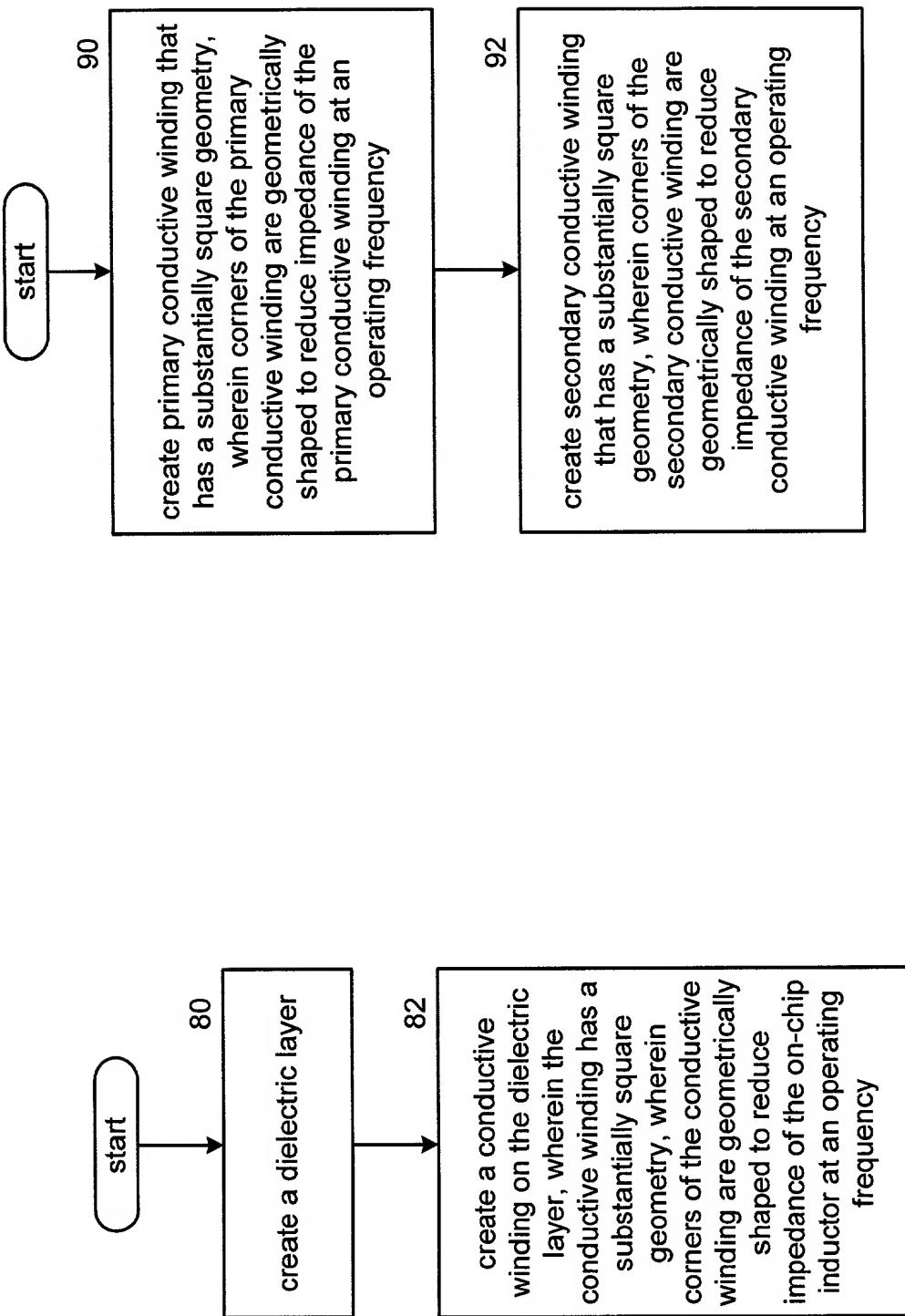
**FIG. 6A**  
front view



**FIG. 6B**  
side view



**FIG. 8**



**FIG. 9**